

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Nakagawa, K.

Serial No.: 09/688,203

Group Art Unit: 2823

Filing Date: October 16, 2000

Examiner: Collins, D.

For: METHOD OF MANUFACTURING SEMICONDUCTOR MEMORY DEVICE

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

RECEIVED

TECHNOLOGY CENTER 2800
APR 15 2002

NOTICE OF CHANGE OF ADDRESS

Sir:

Please be advised that the correspondence address of attorneys of record in the above-identified application has been changed to:

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Please change the records in regard to the above-identified application accordingly and direct all telephone calls to the number shown below.

Respectfully submitted,



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Date: 4/12/02

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